

CLAIMS:

Sw A. 1. A method of displaying a video signal (Vs) with video lines in a video field period on a plasma display panel (1) having a first and a second display field of display lines, the display lines (Di) of the first display field being in an interlaced position with respect to the display lines (Di) of the second display field, the method comprising the steps of:

5 alternately selecting (2) several times the first display field only, or the second display field only, both during respective time periods which are longer than the video field period, and

supplying (3) video data signals (Dsj) in conformance with the video lines to the display lines (Di) of the selected display field.

10 2. A method as claimed in claim 1, characterized in that the number of video lines in a video field period is smaller than or substantially equal to the number of display lines (Di) of the first or second display field.

15 3. A method as claimed in claim 1, characterized in that the time periods are substantially longer than the video field period.

20 4. A circuit for displaying a video signal (Vs) with video lines in a video field period on a plasma display panel (1) having a first and a second display field of display lines, the display lines (Di) of the first display field being in an interlaced position with respect to the display lines (Di) of the second display field, the circuit comprising:

means (2) for alternately selecting several times the first display field only, or the second display field only, both during respective time periods which are longer than the video field period, and

25 means (3) for supplying video data signals (Dsj) in conformance with the video lines to the display lines (Di) of the selected display field.

5. A plasma display device comprising a plasma display panel (1) and a circuit for displaying a video signal (Vs) with video lines in a video field period on a plasma display panel (1) having a first and a second display field of display lines, the display lines (Di) of the first display field being in an interlaced position with respect to the display lines (Di) of the

5 second display field, the circuit comprising:

means (2) for alternately selecting several times the first display field only, or the second display field only, both during respective time periods which are longer than the video field period, and

means (3) for supplying video data signals (Dsj) in conformance with the video
10 lines to display lines (Di) of the selected display field.

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